

SPI 2017

21st IEEE WORKSHOP ON
SIGNAL AND POWER INTEGRITY

Final Schedule

7-10 MAY 2017, LAKE MAGGIORE (BAVENO), ITALY

	Sunday May 7	Monday May 8	Tuesday May 9	Wednesday May 10
08:00		8:00 Registration opens	8:00 Registration opens	
		08:30 - 08:50 Opening Ceremony	08:30 - 09:30 SESSION 4: Measurements & Characterization	8:30 Registration opens
09:00		08:50 - 09:40 Keynote		09:00 - 10:00 SESSION 8: Transmission Line Modeling
10:00		09:40 - 10:40 SESSION 1: Noise Analysis & Reduction Techniques	09:30 - 10:30 SESSION 5: Power Delivery Networks	10:00 - 10:40 SESSION 9: TSV & 3DIC
		10:40 - 11:10 Break	10:30 - 11:00 Break	10:40 - 11:10 Break
11:00		11:10 - 12:30 SESSION 2: Stochastic Analysis & Uncertainty Quantification	11:00 - 12:30 SESSION 6: Macromodeling & MOR	11:10 - 12:10 SESSION 10: Optical & Nano Interconnects
12:00		12:30 - 14:00 Lunch	12:30 - 14:00 Lunch	12:10 - 12:30 Closing ceremony
13:00				
14:00	14:00 - 15:30 TUTORIAL: Substrate Integrated Waveguide (SIW) Technology - Part I	14:00 - 15:00 SESSION 3: Electromagnetic Modeling	14:00 - 15:20 SESSION 7: High-Speed Link Design and Modeling	IBIS Summit
15:00	15:30 - 16:00 Break	15:00 - 17:30 Poster session (includes coffee break)	15:30 - 16:00 Break	
16:00	16:00 - 17:30 TUTORIAL: Substrate Integrated Waveguide (SIW) Technology - Part II		16:00 - 17:30 Industry Forum and Panel Discussion	
17:00				
18:00	Welcome reception	Free dinner	Gala Dinner	
19:00				
20:00				
21:00				
22:00				